

**INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE**  
**Department of Electrical Engineering**

Dated: January 25, 2019

**ADVERTISEMENT TO FILL UP PROJECT POSITIONS\***

Applications are invited from Indian nationals only for project position(s) as per the details given below for the research project under the Principal investigator Dr. Vishal Kumar, Electrical Engineering Department, IIT, Roorkee.

1. Title of project: RECONFIGURABLE IPs LIBRARY FOR POWER SYSTEM PROTECTION
  2. Sponsor of the project: Department of Science and Technology (DST), India
  3. Project position(s) and number:  
JRF - 2  
Project Attendant - 1
  4. Qualifications:  
JRF -M. Tech in Electrical/Electronic Engineering (with the knowledge of HDL & FPGAs) and GATE Qualified  
Project Attendant- 12th pass or 10th pass + 2 year experience or 8th Pass + 4 year experience
  5. Emoluments:  
JRF -Rs. 25000/- PM  
Project Attendant- Rs. 10000/- PM
  6. Duration: 3 Years (up to duration of the project)
  7. Job description  
JRF –Simulations & Implementation of FPGA targeted designs, and operational testing of developed prototypes under real time simulation environment  
Project Attendant- Taking care of all the equipments of the laboratory, performing the office work, and the work assigned by the PIs.
1. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
  2. Candidates desiring to appear for the Interview should submit their applications with the following documents to the office of Principal Investigator through email, by post or produce at the time of Interview:
    - Application in a plain paper with detailed CV including chronological discipline of degree/certificates obtained.
    - Experience including research, industrial field and others.
    - Attested copies of degree/certificate and experience certificate.
  3. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
  4. Preference will be given to SC/ST candidates on equal qualifications and experience.
  5. Please note that no TA/DA is admissible for attending the interview.

**Note: The selected candidate for JRF may get an opportunity for PhD admission.**

The interview will be held at 3:30 PM on February 21, 2018 at Department of Electrical Engineering, IIT Roorkee

**Tel: +919412307677**

**Email: [vkaxfee@iitr.ac.in](mailto:vkaxfee@iitr.ac.in)**

  
**Vishal Kumar**  
**Principal Investigator**

**\*\*Up load at Channel I and Institute website**